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FILING DATE APPLICATION NO. FIRST NAMED INVENTOR ATTORNEY DOCKET NO. CONFIRMATION NO. 10/092,526 03/08/2002 Hisao Shigematsu 981380A 4590 38834 **EXAMINER** 7590 08/04/2005 WESTERMAN, HATTORI, DANIELS & ADRIAN, LLP RICHARDS, N DREW 1250 CONNECTICUT AVENUE, NW ART UNIT PAPER NUMBER **SUITE 700** WASHINGTON, DC 20036 2815

DATE MAILED: 08/04/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

••			SP
	Application No.	Applicant(s)	
Office Action Summary	10/092,526	SHIGEMATSU ET AL	
	Examiner	Art Unit	
	N. Drew Richards	2815	•
The MAILING DATE of this communication a Period for Reply	appears on the cover sheet w	ith the correspondence addre	ss
A SHORTENED STATUTORY PERIOD FOR REF THE MAILING DATE OF THIS COMMUNICATION - Extensions of time may be available under the provisions of 37 CFR after SIX (6) MONTHS from the mailing date of this communication. - If the period for reply specified above is less than thirty (30) days, a r - If NO period for reply is specified above, the maximum statutory perion - Failure to reply within the set or extended period for reply will, by stall Any reply received by the Office later than three months after the may earned patent term adjustment. See 37 CFR 1.704(b).	N. 1.136(a). In no event, however, may a reply within the statutory minimum of thir od will apply and will expire SIX (6) MON tute, cause the application to become Al	reply be timely filed ty (30) days will be considered timely. NTHS from the mailing date of this commi	unication.
Status	•		
1) Responsive to communication(s) filed on 20	May 2005.		
2a) ☐ This action is FINAL . 2b) ☑ The	his action is non-final.		
3) Since this application is in condition for allow	·	•	erits is
closed in accordance with the practice unde	r <i>Ex parte Quayle</i> , 1935 C.D). 11, 453 O.G. 213.	
Disposition of Claims			
4) ⊠ Claim(s) 13-24 and 26 is/are pending in the 4a) Of the above claim(s) 14,16,19,22-24 and 5) □ Claim(s) is/are allowed. 6) ⊠ Claim(s) 13,15,17,18,20 and 21 is/are reject 7) □ Claim(s) is/are objected to. 8) □ Claim(s) are subject to restriction and	<u>d 26</u> is/are withdrawn from c	onsideration.	
Application Papers			
9) The specification is objected to by the Exami 10) The drawing(s) filed on <u>08 March 2002</u> is/are Applicant may not request that any objection to the Replacement drawing sheet(s) including the corre 11) The oath or declaration is objected to by the	e: a)⊠ accepted or b)⊡ obj ne drawing(s) be held in abeyar ection is required if the drawing	nce. See 37 CFR 1.85(a). (s) is objected to. See 37 CFR 1	, ,
Priority under 35 U.S.C. § 119			
12) Acknowledgment is made of a claim for foreign a) All b) Some * c) None of: 1. Certified copies of the priority docume 2. Certified copies of the priority docume 3. Copies of the certified copies of the priority docume application from the International Bure * See the attached detailed Office action for a list	ents have been received. Ents have been received in A Tiority documents have been Eau (PCT Rule 17.2(a)).	pplication No. <u>09/191,543</u> . received in this National Sta	ge
Attachment(s)			
 Notice of References Cited (PTO-892) Notice of Draftsperson's Patent Drawing Review (PTO-948) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/0 Paper No(s)/Mail Date 	Paper No(s	Summary (PTO-413) s)/Mail Date nformal Patent Application (PTO-152 	2)

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DETAILED ACTION

Election/Restrictions

- 1. Applicant's election of Species I, claims 13, 15, 17, 20, 21, and 25 in Paper No. 6 is acknowledged. Because applicant did not distinctly and specifically point out the supposed errors in the restriction requirement, the election has been treated as an election without traverse (MPEP § 818.03(a)). In response to the Office Action mailed 4/22/05, applicant has pointed out that claim 18 is also part of the elected species. Thus, claims 13, 15, 17, 18, 20, 21 and 25 are considered herein.
- 2. Claims 14, 16, 19, 22 24, and 26 are withdrawn from further consideration pursuant to 37 CFR 1.142(b) as being drawn to a nonelected species, there being no allowable generic or linking claim. Election was made **without** traverse in Paper No. 6.

Claim Objections

3. Claim 18 is objected to under 37 CFR 1.75(c), as being of improper dependent form for failing to further limit the subject matter of a previous claim. Applicant is required to cancel the claim(s), or amend the claim(s) to place the claim(s) in proper dependent form, or rewrite the claim(s) in independent form. Applicant added the subject matter of claim 18 into claim 13, claim 18 no longer further limits the scope of claim 13.

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4. Applicant is advised that should claim 13 be found allowable, claim 18 will be objected to under 37 CFR 1.75 as being a substantial duplicate thereof. When two claims in an application are duplicates or else are so close in content that they both cover the same thing, despite a slight difference in wording, it is proper after allowing one claim to object to the other as being a substantial duplicate of the allowed claim. See MPEP § 706.03(k).

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Claim Rejections - 35 USC § 103

- 5. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
 - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 6. Claims 13, 15, 17, 18 and 21 are rejected under 35 U.S.C. 103(a) as being unpatentable over Shimawaki (USPAT 5903018) in view of Tanoue et al. (USPAT 5598015, Tanoue) further in view of Mochizuki et al. (USPAT 5481120, Mochizuki).

With regard to claim 13, Shimawaki discloses in figures 3 – 7 a method for fabricating a semiconductor device. Shimawaki discloses in figure 3 forming a first semiconductor layer (4) over a semiconductor substrate (1). Shimawaki teaches in figure 3 and column 5, lines 31 – 32 wherein the semiconductor substrate is made of a GaAs semiconductor substrate. Shimawaki is silent to the substrate being formed of an InP semiconductor substrate. Tanoue teaches in figures 1 – 11 and column 3, lines 30 – 31 wherein a first semiconductor layer (2/3) is formed over an InP substrate. It would

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have been obvious to one of ordinary skill in the art at the time of the present invention to use the InP substrate of Tanoue in the method of Shimawaki in order to increase the cutoff frequency of the device by selecting materials that can be used for the same purpose as stated by Tanoue in column 5, lines 27 - 42, column 5, lines 48 - 49, and column 6, lines 9 – 29. (See MPEP 2144.06 and 2144.07). Shimawaki discloses in figure 3 and column 5, lines 29 – 55 and column 7, lines 54 – 60 forming a base layer (5) of a carbon doped Ga_xIn_{1-x}As_ySb_{1-y} layer on the first semiconductor layer. Shimawaki discloses in figure 3 forming a second semiconductor layer (7) on the base layer. Shimawaki discloses in figure 4 patterning the second semiconductor layer in a mesa shape. Shimawaki discloses in figure 6 and column 6, lines 40 - 55 forming a base contact layer (12) on the base layer exposed by patterning the second semiconductor layer. Shimawaki is silent to the base contact layer being of a carbon-doped GaAsSb layer or a carbon doped GalnAsSb layer. Mochizuki teaches in figure 6 and column 11 lines 1-20 forming a carbon-doped GaAsSb base contact layer 16 on a GaAs base layer. It would have been obvious to one of ordinary skill in the art at the time of the present invention to use the GaAsSb base contact layer of Mochizuki in the method of Shimawaki to improve the carrier concentration and mobility in the base region so that the base resistance is reduced and a very high speed HBT is realized. Shimawaki discloses in figure 6 forming a base electrode 14 on the base contact layer. Shimawaki discloses in figure 7 and column 5, lines 29 – 55 wherein the second semiconductor layer is an emitter layer of an AlGaAs layer. Shimawaki does not teach that the emitter layer is of an InP layer. Tanoue teaches in figures 1 – 11 and column 3, lines 30 – 31 in

which a second semiconductor layer is an emitter layer of an InP layer. It would have been obvious to one of ordinary skill in the art at the time of the present invention to use the InP emitter of Tanoue in the method of Shimawaki in order to increase the cutoff frequency of the device by selecting materials that can be used for the same purpose as stated by Tanoue in column 5, lines 27 – 42, column 5, line 51, and column 6, lines 9 – 29. (See MPEP 2144.06 and 2144.07).

With regard to claim 15, Shimawaki discloses in figure 3 and column 6, lines 5 – 6 wherein in the step of forming the base layer, the base layer of an InGaAs layer which corresponds to the Ga_xIn_{1-x}As_ySb_{1-y} layer whose As composition y is 1.

With regard to claim 17, Shimawaki discloses in figure 6 and column 6, lines 40 – 55 wherein in the step of forming the base contact layer, the base contact layer is formed of a material which lattice matches with a material forming the base layer. It should be noted that lattice matching results from the MOMBE (metal organic molecular beam epitaxy) process used to form the base contact layer.

With regard to claim 18, Shimawaki is silent to the base contact layer being of a carbon-doped GaAsSb layer or a carbon doped GaInAsSb layer. Mochizuki teaches in figure 6 and column 11 lines 1-20 forming a carbon-doped GaAsSb base contact layer 16 on a GaAs base layer. It would have been obvious to one of ordinary skill in the art at the time of the present invention to use the GaAsSb base contact layer of Mochizuki in the method of Shimawaki to improve the carrier concentration and mobility in the base region so that the base resistance is reduced and a very high speed HBT is realized.

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With regard to claim 21, Shimawaki discloses in figure 5 after the step of patterning the second semiconductor layer, a step of forming a sidewall insulation film (18) on a sidewall of a mesa of the second semiconductor layer.

7. Claim 20 is rejected under 35 U.S.C. 103(a) as being unpatentable over Shimawaki and Tanoue as applied to claim 13 above, and further in view of Hashimoto et al. (USPAT 5846869, Hashimoto).

Shimawaki discloses in figure 6 and column 6, lines 1 – 13 and 50 – 51 depositing the base layer by MOCVD epitaxial deposition process. Shimawaki and Tanoue are silent to, before the step of forming the base contact layer, a step of thermal treating for eliminating hydrogen in the base layer. Hashimoto teaches in figures 18 – 20 and column 11, line 56 – column 12, line 31, before the step of forming a layer overlying a base layer, a step of thermal treating for eliminating hydrogen in the base layer introduced during the deposition of the base layer by an epitaxial process. It would have been obvious to one of ordinary skill in the art at the time of the present invention to use the thermal treating of Hashimoto before the step of forming the base contact layer in the method of Shimawaki and Tanoue in order to improve the amplification factor of the bipolar transistor as stated by Hashimoto in column 11, line 56 – column 12, line 31. It would have been further obvious in the method of Shimawaki, Tanoue, and Hashimoto the eliminated hydrogen would have been due to the epitaxial MOCVD process of Shimawaki. It should further be noted that the limitation "for eliminating

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hydrogen" is an intended use limitation that is met by the combination of Shimawaki, Tanoue, and Hashimoto.

Response to Arguments

8. Applicant's arguments filed 2/14/05 have been fully considered but they are not persuasive.

Applicant's arguments regarding the material of the base contact layer are moot in view of the new grounds of rejection presented above.

Applicant argues that one of ordinary skill in the art would not have formed the AlGaAs/GaAs-based HBT of Shimawaki on the InP substrate of Tanoue since the GaAs substrate must be used in order to lattice-match the HBT layers with the substrate. This is not persuasive. The fact that one might, in one instance, not form the AlGaAs/GaAS-based HBT on InP because of lattice matching issues does not preclude one of ordinary skill in the art being motivated to perform the combination in another instance. In this case proper motivation has been given as to why one would desire the combination and thus the rejection is considered proper.

In response to applicant's arguments against the references individually, one cannot show nonobviousness by attacking references individually where the rejections are based on combinations of references. See *In re Keller*, 642 F.2d 413, 208 USPQ 871 (CCPA 1981); *In re Merck & Co.*, 800 F.2d 1091, 231 USPQ 375 (Fed. Cir. 1986).

With regard to claim 20, applicant has argued that the thermal treatment of Hashimoto is conducted for a different reason than in the instant application and thus does not read on the thermal treatment as claimed. This is not persuasive since the fact that applicant has recognized another advantage which would flow naturally from following the suggestion of the prior art cannot be the basis for patentability when the differences would otherwise be obvious. See *Ex parte Obiaya*, 227 USPQ 58, 60 (Bd. Pat. App. & Inter. 1985). In this case, the thermal treatment of Hashimoto is desirable to improve the amplification factor of the bipolar transistor as stated by Hashimoto in column 11, line 56 – column 12, line 31. In performing this treatment, the claimed hydrogen elimination will necessarily occur and thus the thermal treatment of Hashimoto in the method of Shimawaki and Tanoue reads on the claimed invention.

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Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to N. Drew Richards whose telephone number is (571) 272-1736. The examiner can normally be reached on Monday-Friday 9:00-5:00.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Tom Thomas can be reached on (571) 272-1664. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

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N. Drew Richards

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